
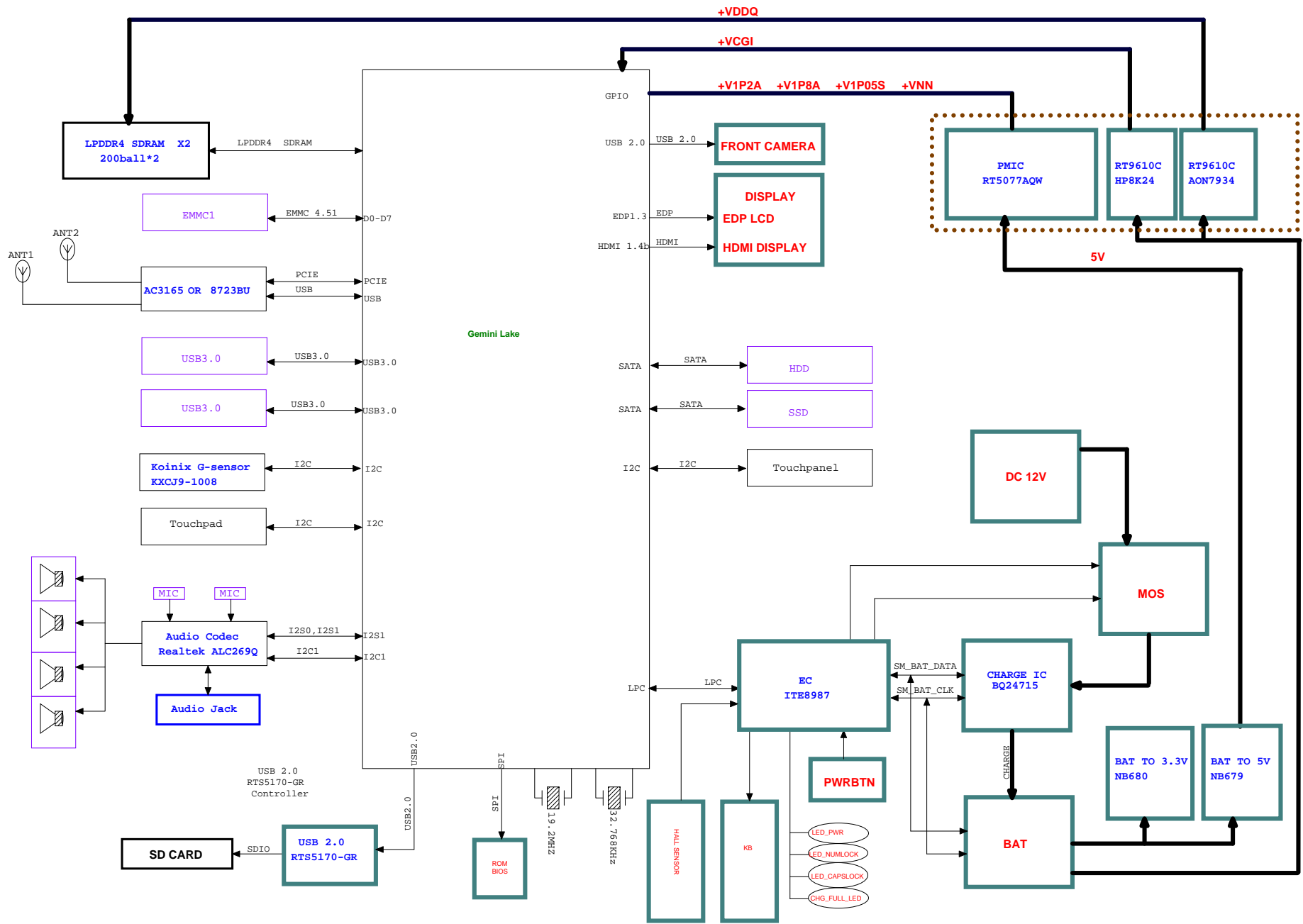


Gemini Lake

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CG14D01 Circuit diagram



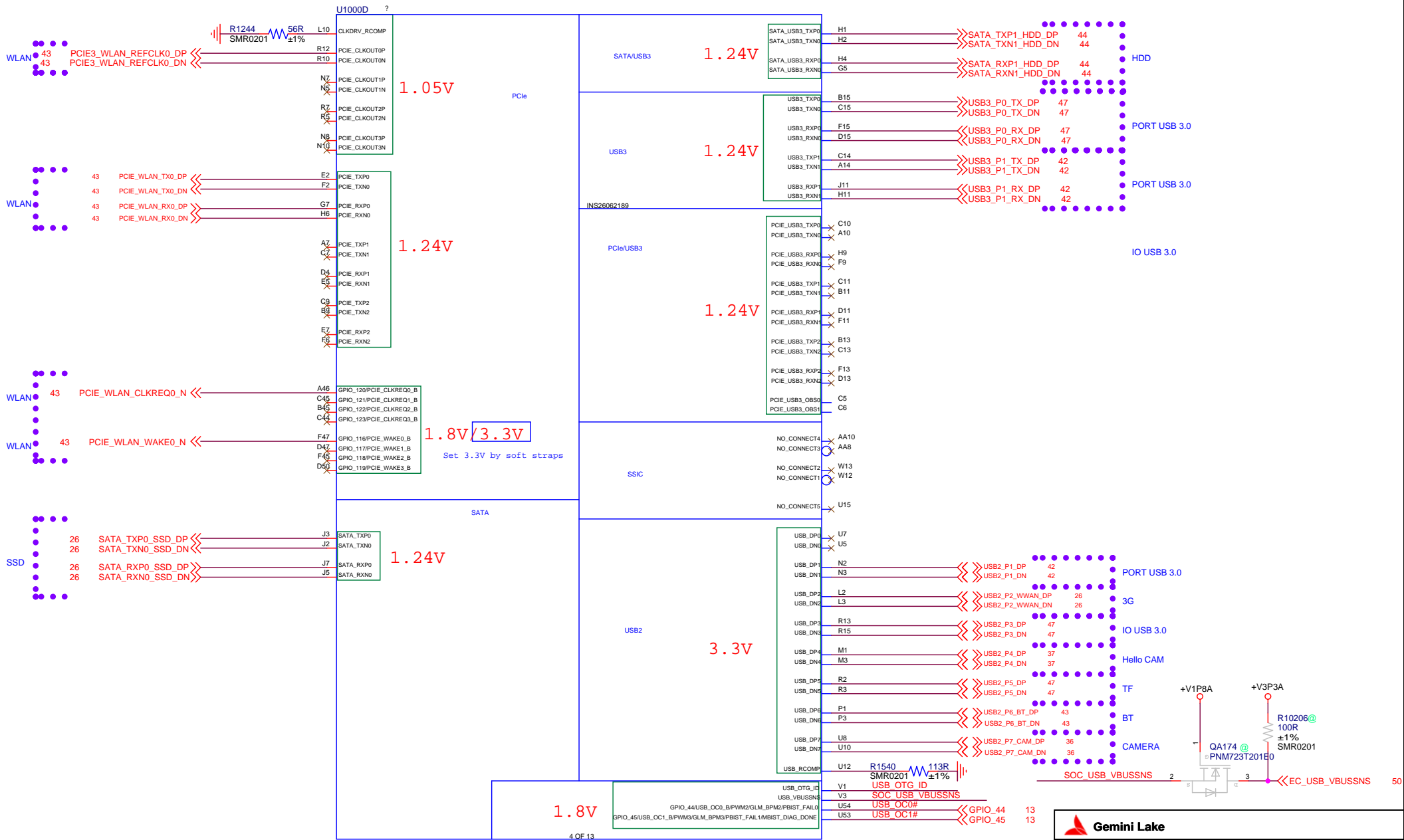
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BD3	DDR1CH0DQ1_DQ41_DQ89_DQ89	DDR1CH0DQSN0_DQSN0_DQSPA0_DQSPA0	BK25
BD1	DDR1CH0DQ2_DQ42_DQ810_DQ810		
BC3	DDR1CH0DQ3_DQ43_DQ811_DQ811	DDR1CH0DQSP1_DQSP1_DQSPA1_DQSPA1	BD25
AY1	DDR1CH0DQ4_DQ44_DQ812_DQ812	DDR1CH0DQSN1_DQSN1_DQSPA1_DQSPA1	BF25
BA3	DDR1CH0DQ5_DQ45_DQ813_DQ813		
BA2	DDR1CH0DQ6_DQ46_DQ814_DQ814	DDR1CH0DQSP2_DQSP2_DQSPA2_DQSPA2	BL18
BE2	DDR1CH0DQ7_DQ47_DQ815_DQ815	DDR1CH0DQSN2_DQSN2_DQSPA2_DQSPA2	BL18
AR8	DDR1CH0DQ8_DQ32_DQ80_DQ80		
AN15	DDR1CH0DQ9_DQ33_DQ81_DQ81	DDR1CH0DQSP3_DQSP3_DQSPA3_DQSPA3	AV19
AN7	DDR1CH0DQ10_DQ34_DQ82_DQ82	DDR1CH0DQSN3_DQSN3_DQSPA3_DQSPA3	AV21
AU12	DDR1CH0DQ11_DQ35_DQ83_DQ83		
AN2	DDR1CH0DQ12_DQ36_DQ84_DQ84	DDR1CH0DQSP1_DQSP4_DQSPB0_DQSPB0	AR13
AN3	DDR1CH0DQ13_DQ37_DQ85_DQ85	DDR1CH0DQSN1_DQSN4_DQSNB0_DQSNB0	AR15
AU13	DDR1CH0DQ14_DQ38_DQ86_DQ86		
AU15	DDR1CH0DQ15_DQ39_DQ87_DQ87	DDR1CH0DQSP0_DQSP5_DQSPB1_DQSPB1	BB3
AP3	DDR1CH0DQ16_DQ56_DQ824_DQ824	DDR1CH0DQSN0_DQSN5_DQSNB1_DQSNB1	BC2
AU2	DDR1CH0DQ17_DQ57_DQ825_DQ825		
AV3	DDR1CH0DQ18_DQ58_DQ826_DQ826	DDR1CH0DQSP3_DQSP6_DQSPB2_DQSPB2	AW7
AV3	DDR1CH0DQ19_DQ59_DQ827_DQ827	DDR1CH0DQSN3_DQSN6_DQSNB2_DQSNB2	AW8
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AR1	DDR1CH0DQ21_DQ61_DQ829_DQ829	DDR1CH0DQSP2_DQSP7_DQSPB3_DQSPB3	AT1
AR3	DDR1CH0DQ22_DQ62_DQ830_DQ830	DDR1CH0DQSN2_DQSN7_DQSNB3_DQSNB3	AT3
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AR5	DDR1CH0DQ24_DQ48_DQ816_DQ816	DDR1CH0CA7_MA0_CAB972_NCNC	BH9
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AU7	DDR1CH0DQ26_DQ50_DQ818_DQ818	DDR1CH0CA8_MA2_CAB554_CAB554	BD11
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		DDR1CH0CA1_BG1_CAB245_CAB222	BG4
		DDR1CH0CA2_ACTN_CAB118_CAB111	BE7
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BZ2	DDR1CH1DQ2_DQ2_DQA2_DQA2		
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BD27	DDR1CH1DQ8_DQ8_DQA8_DQA8	DDR1CH1CA2_BG0_CAA572_CAA555	BJ10
BF27	DDR1CH1DQ9_DQ9_DQA9_DQA9	DDR1CH1CA4_MA4_NCNCNC_NCNC	BJ4
BH27	DDR1CH1DQ10_DQ10_DQA10_DQA10	DDR1CH1CA10_MA5_CAA027_CAA02	BL6
BC27	DDR1CH1DQ11_DQ11_DQA11_DQA11	DDR1CH1CA8_MA6_CAA209_CAA21	BJ5
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AY19	DDR1CH1DQ30_DQ30_DQA30_DQA30	DDR1CH1CSB1_CS1N_CSB1A_CS1A	BC22
BA19	DDR1CH1DQ31_DQ31_DQA31_DQA31	DDR1CH1CKE0_CKE0_CKE0A_CKE0A	BK13
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		DDRRPDEXT_RPDEXT_RPDEXT	AY29 DDR0_RCOMP R6287 SMR0201 110R ±1%
		DDR1RESETB_NC	BC15
		DDR1RPDEXT_NC	AY27 DDR1_RCOMP R6288 SMR0201 110R ±1%
		DDR1VREFCA_VREFCA_NC	AV27
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		DDRRESETB_NC_RESETB	BC43 MO_DRAMRST_N >>> MO_DRAMRST_N 19

Gemini Lake

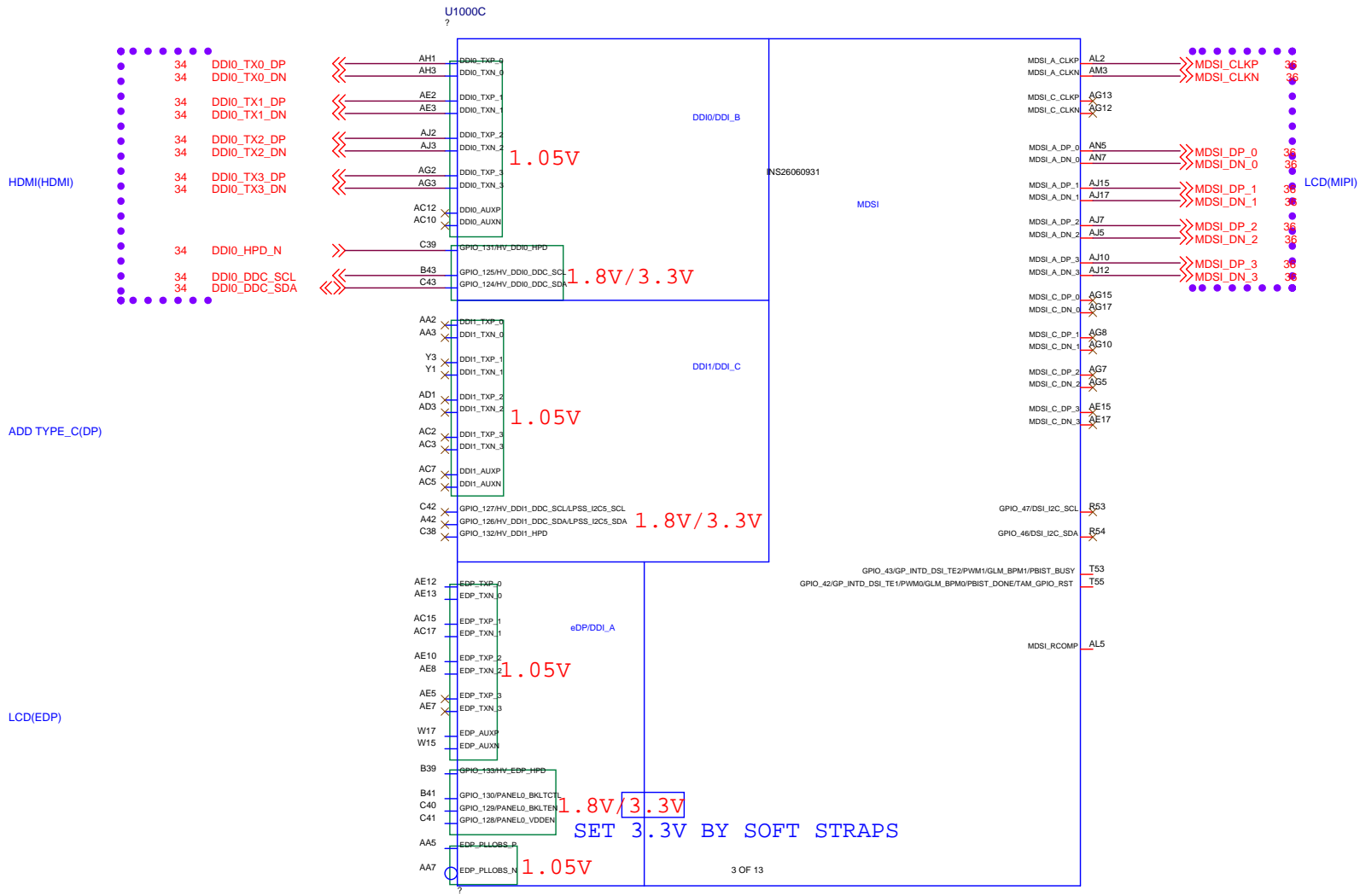
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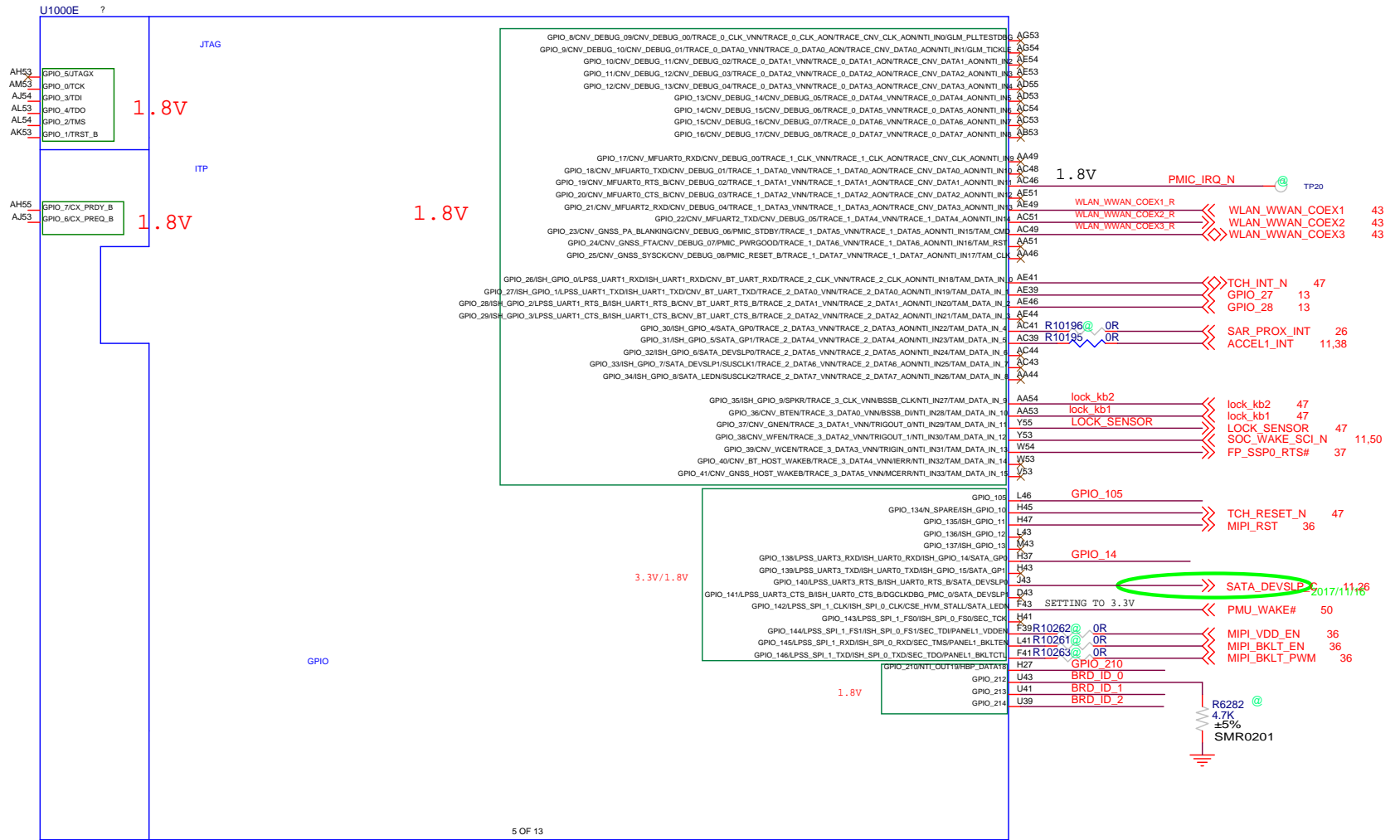
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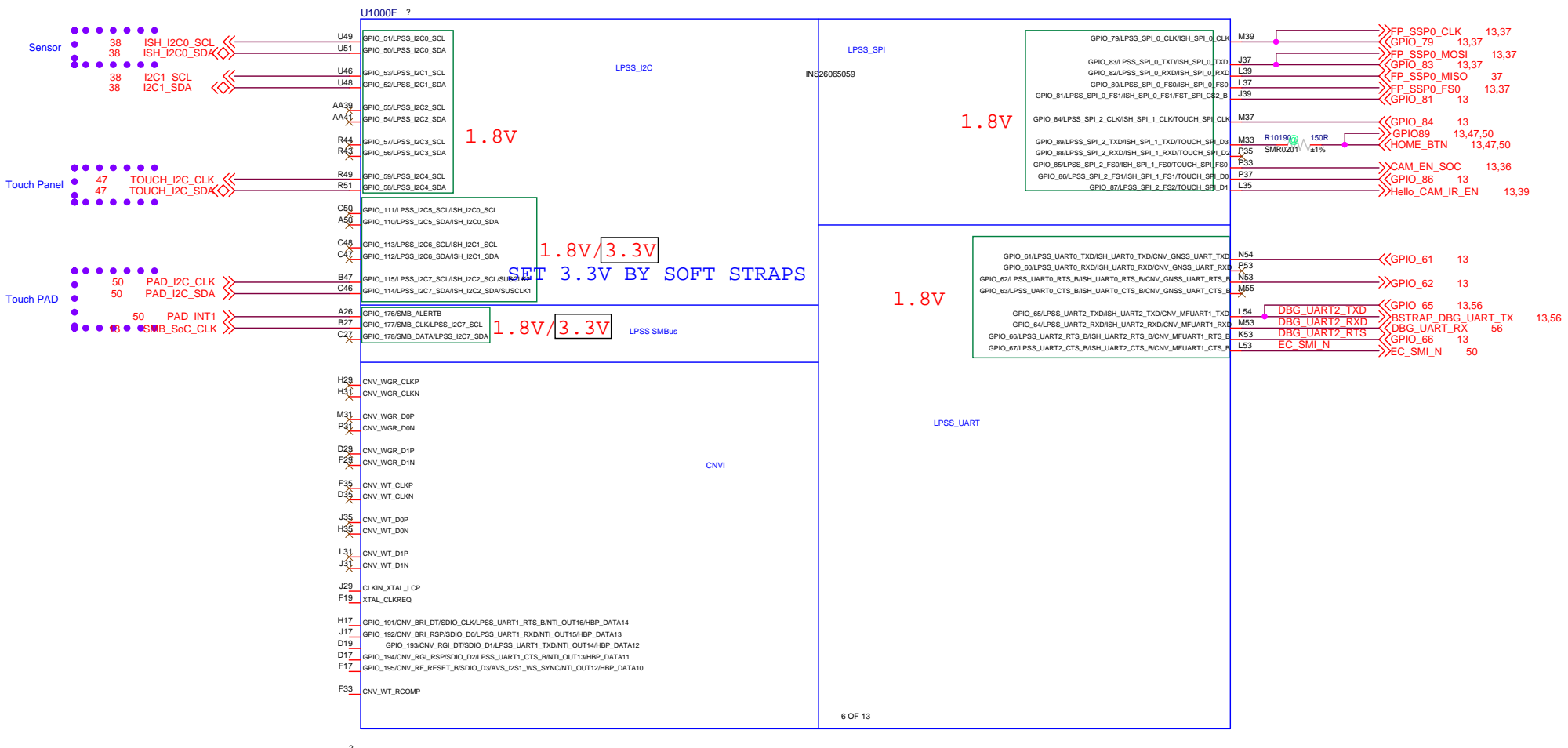
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Gemini Lake

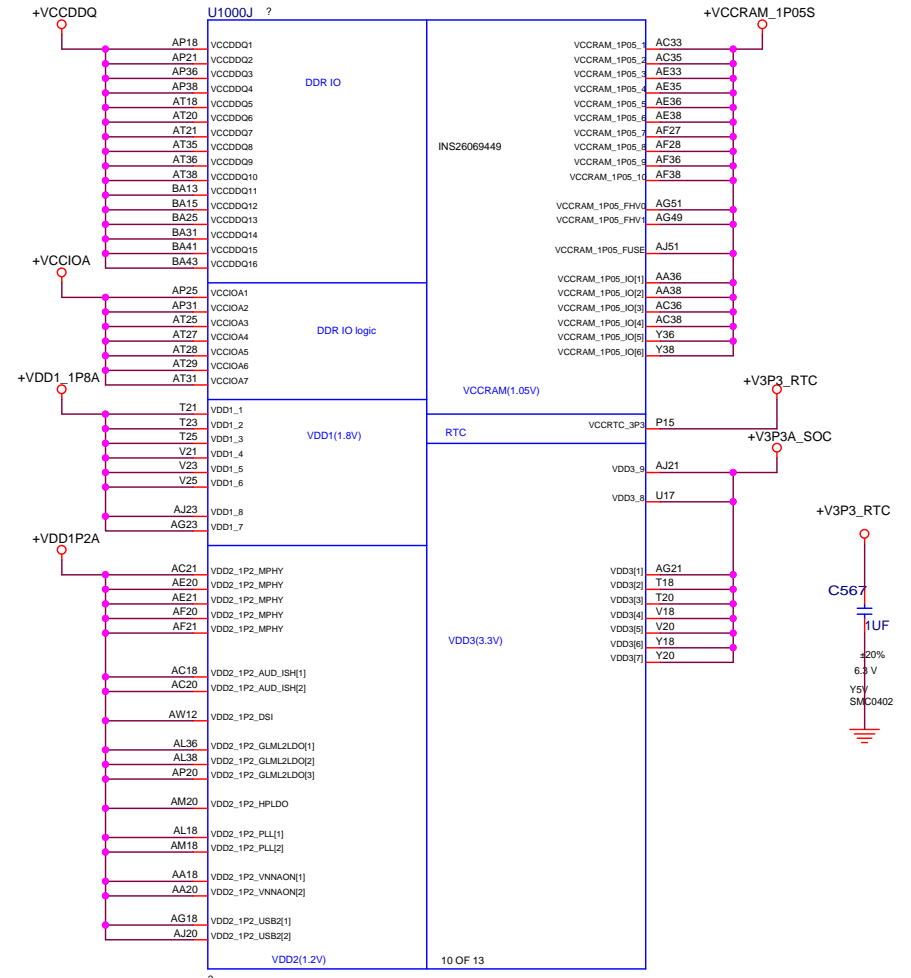
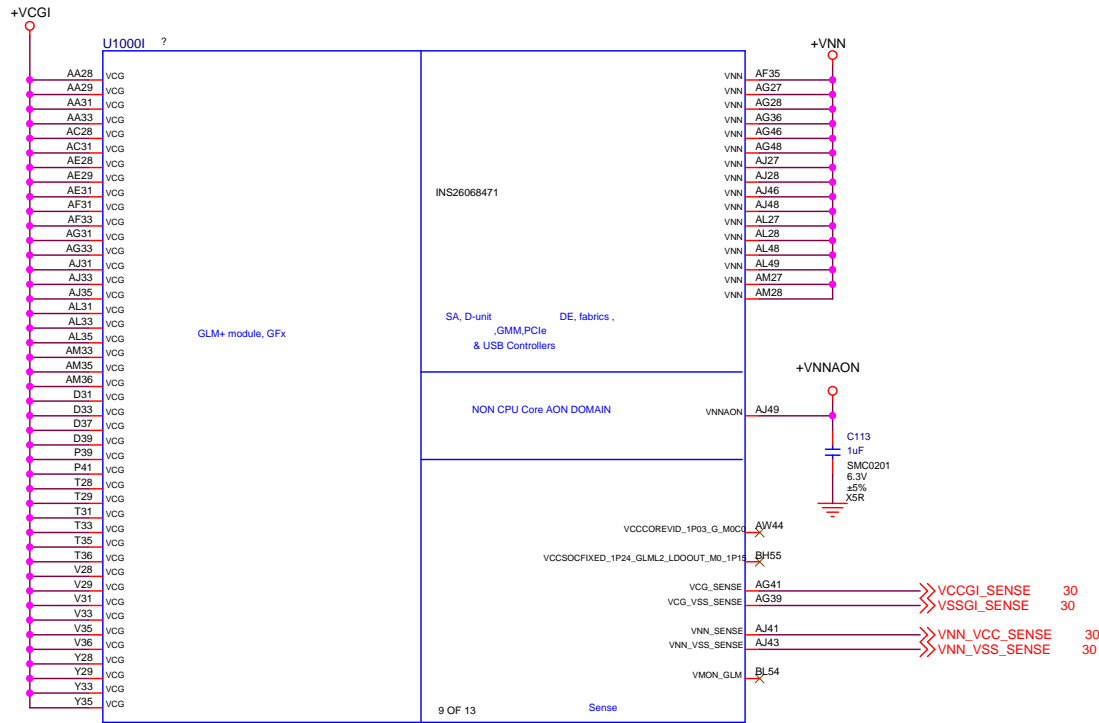
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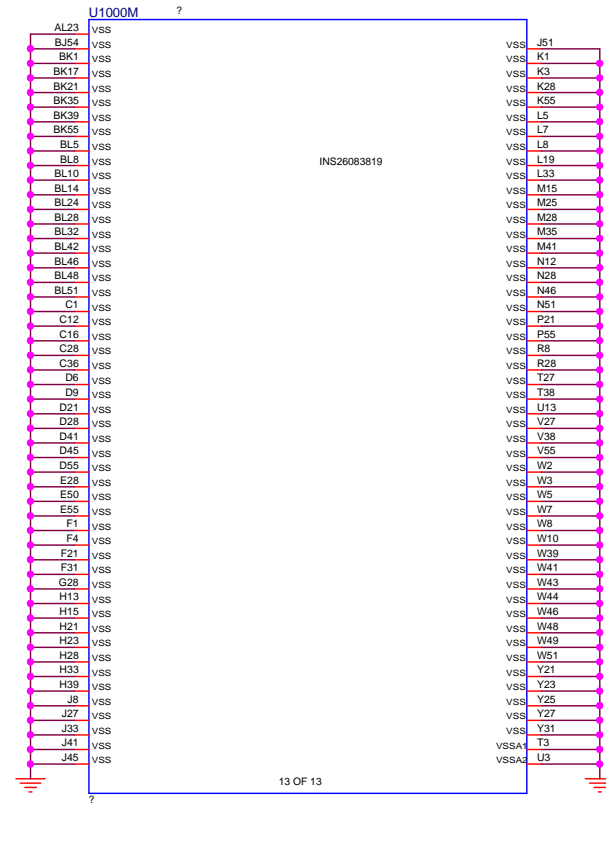
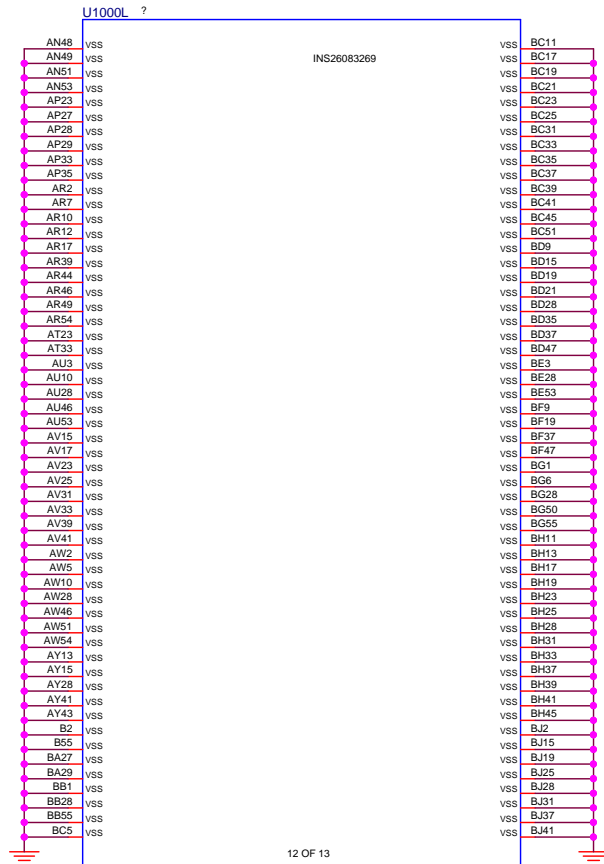
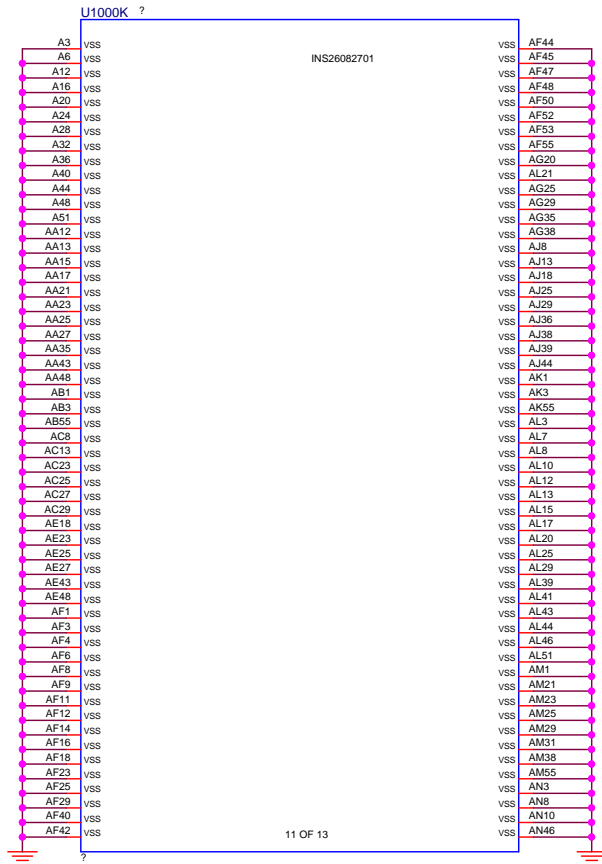
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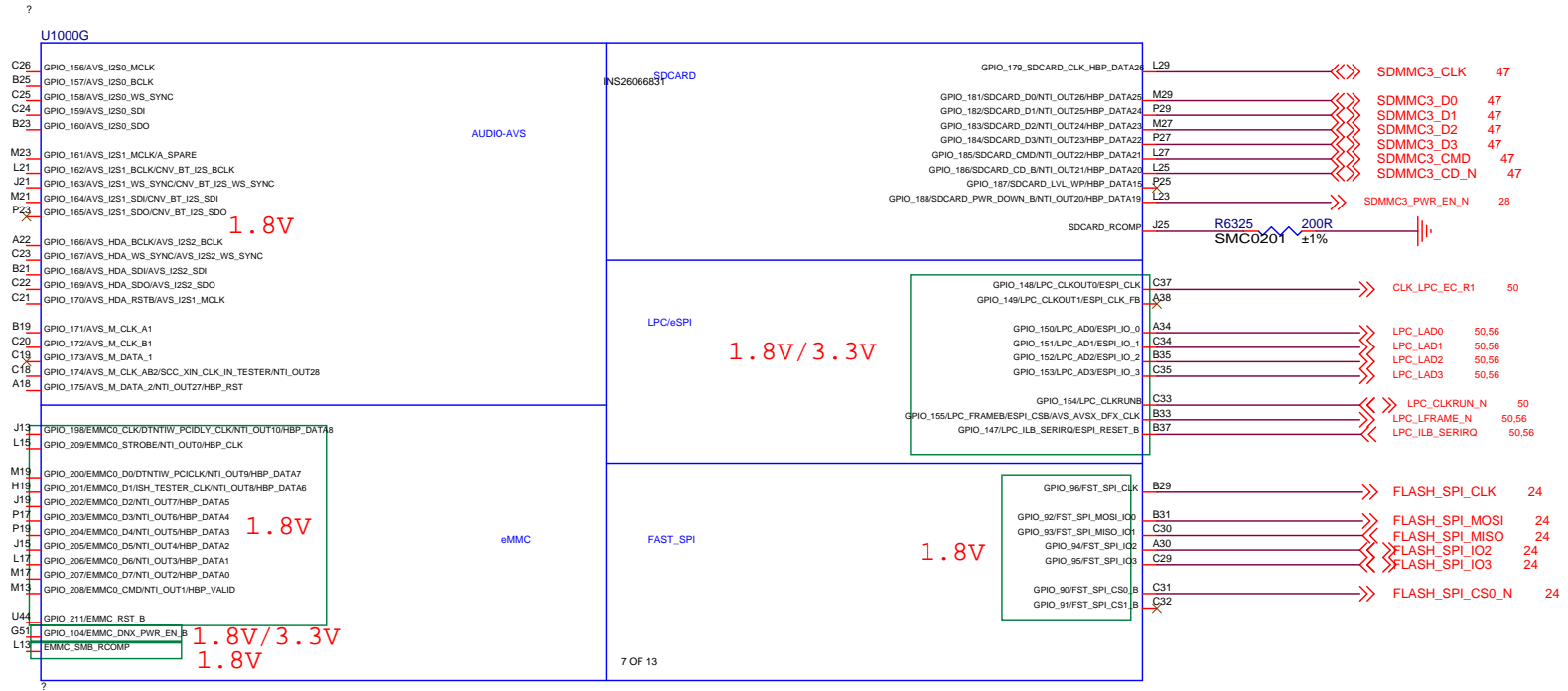


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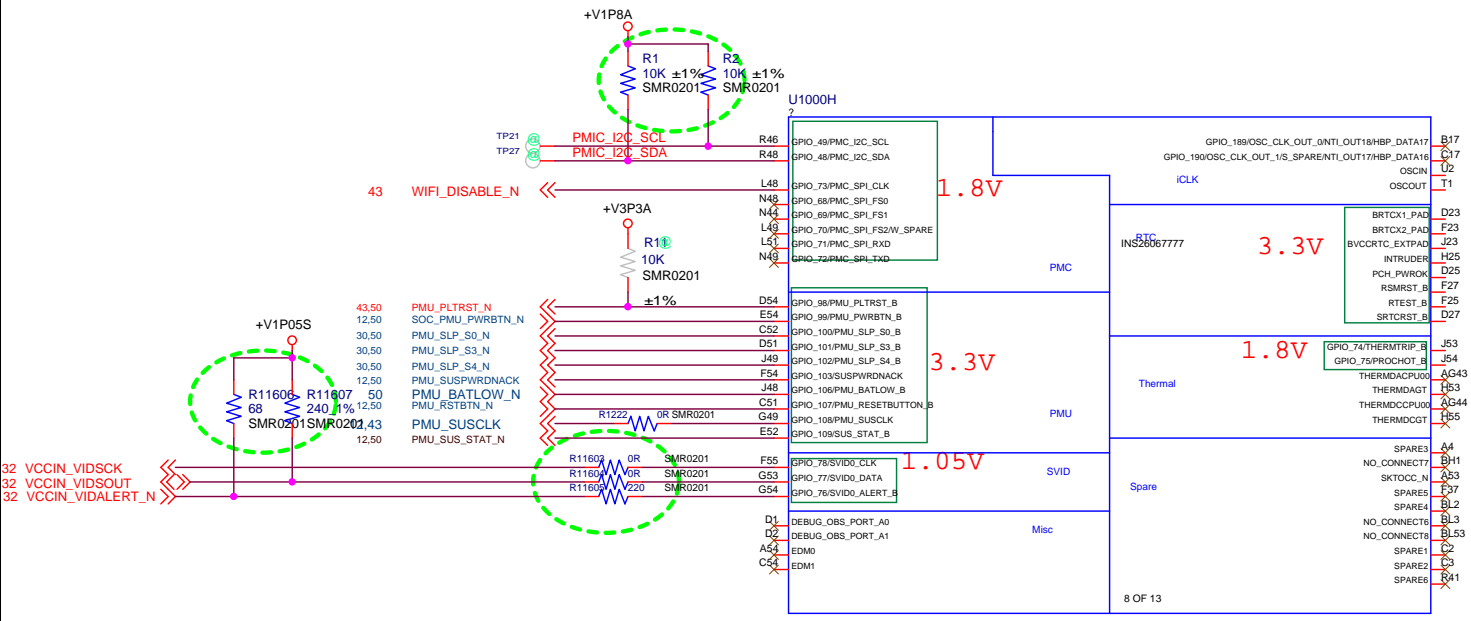


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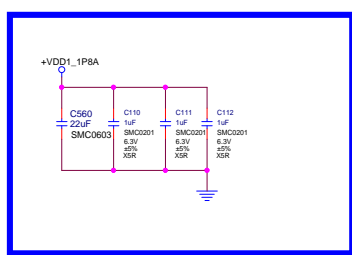
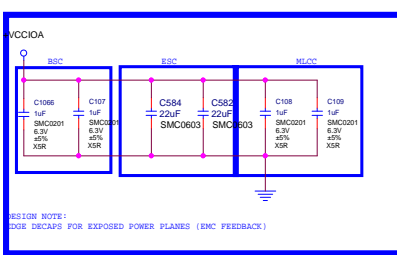
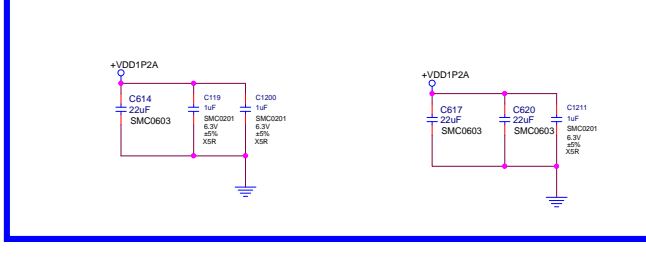
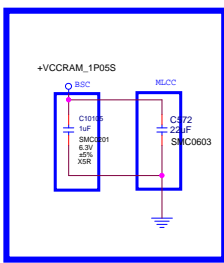
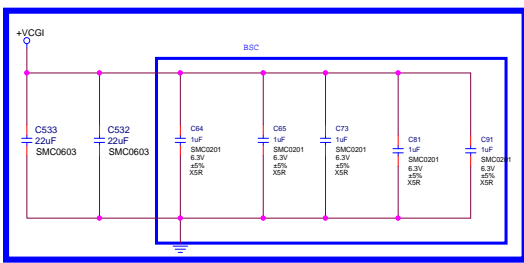
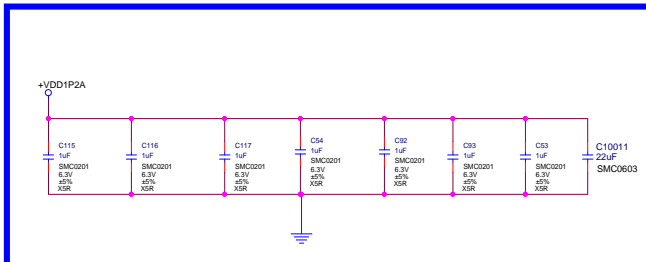
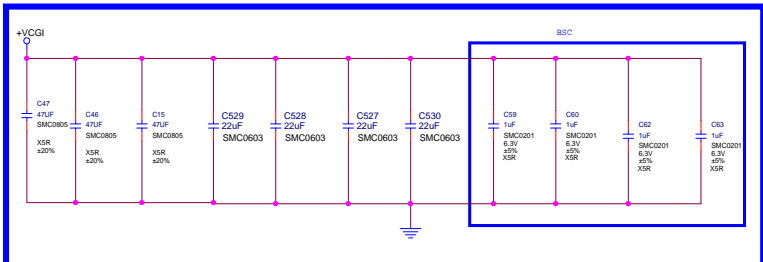
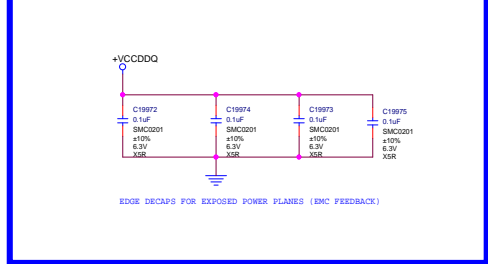
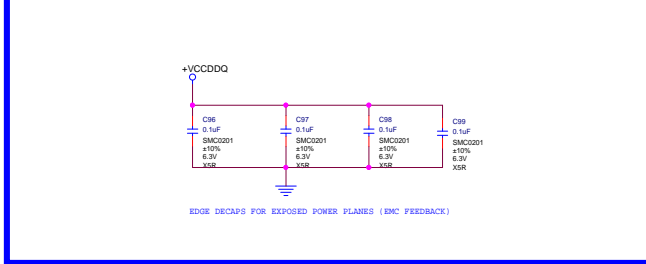
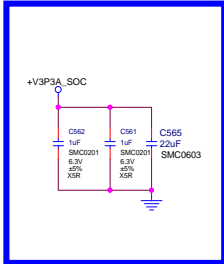
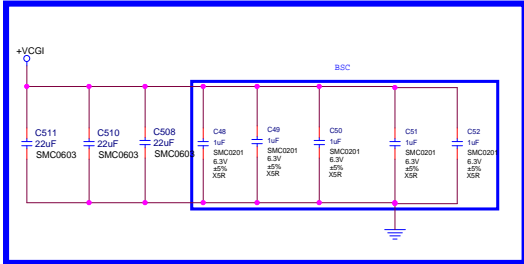
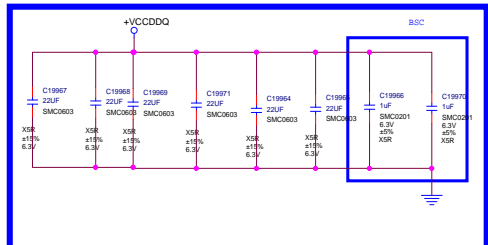
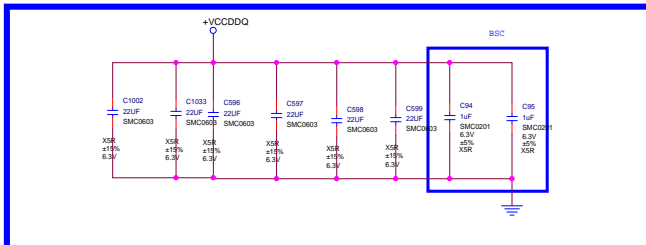
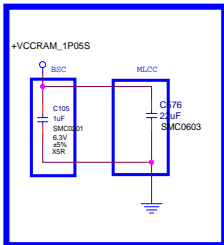
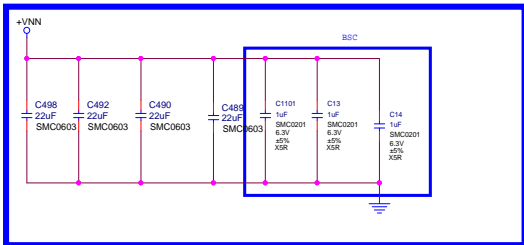




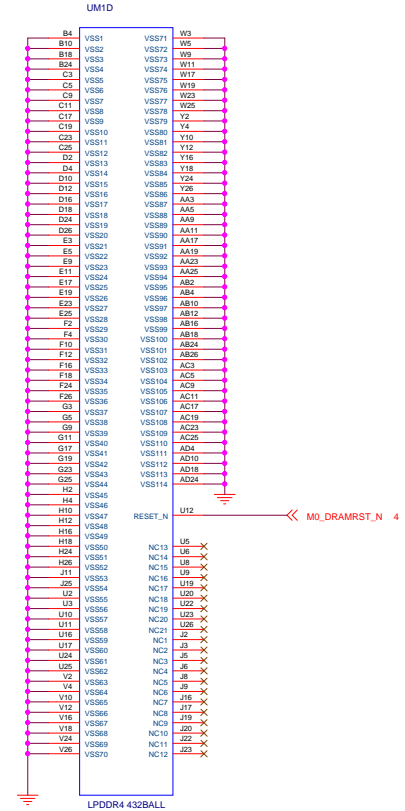
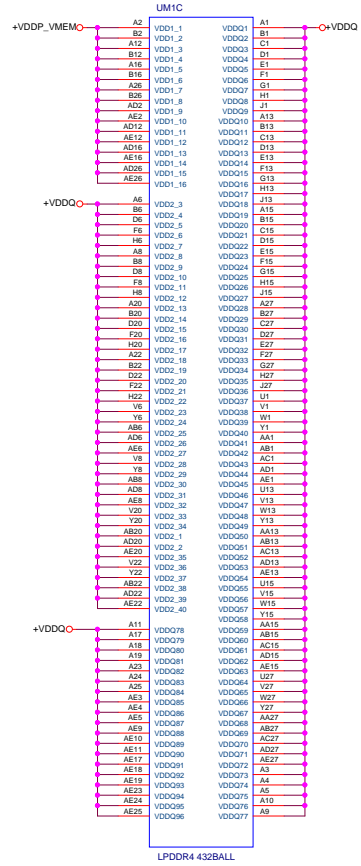
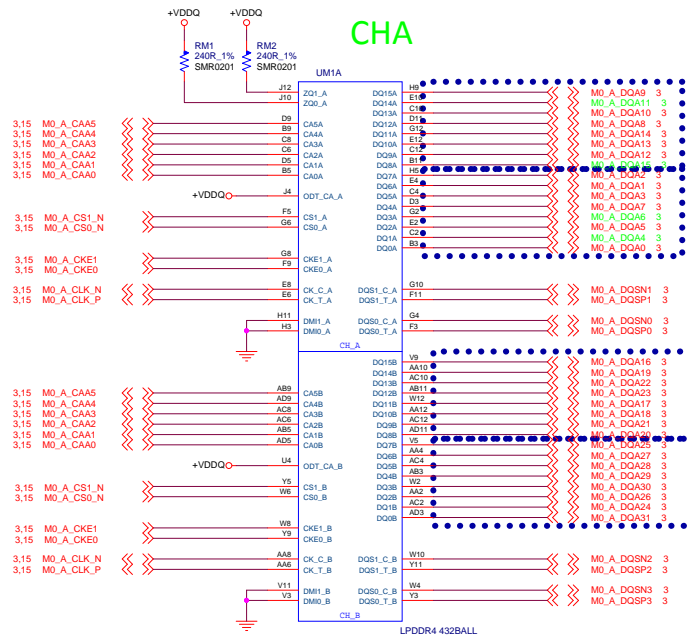
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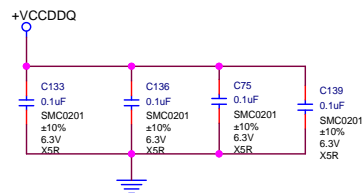
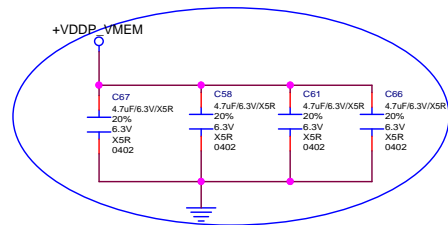


Pin	Signal Name	Function
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C17	GPIO_190/OSC_CLK_OUT_1/S_SPAREN1_OUT17HBP_DATA16	OSCIN
U2	OSCIN	OSCIN
T1	OSCOUT	OSCOUT
D23	BRTCX1_PAD	BRTCX1_PAD
F23	BRTCX2_PAD	BRTCX2_PAD
J23	BVCCRTC_EXTPAD	BVCCRTC_EXTPAD
H25	INTRUDER	INTRUDER
D25	PCH_PWROK	PCH_PWROK
F27	RSMRST_B	RSMRST_B
F25	RTEST_B	RTEST_B
D27	SRTCRST_B	SRTCRST_B
J53	GPIO_74/THERMTRIP_B	THERMTRIP
J54	GPIO_75/PROCHOT_B	PROCHOT
A643	THERMDCP00	THERMDCP00
A63	THERMADGT	THERMADGT
A644	THERMDCCPU0	THERMDCCPU0
A65	THERMDCGT	THERMDCGT
A4	SPARE3	SPARE3
B11	NO_CONNECT7	NO_CONNECT7
A33	SKTOCC_N	SKTOCC_N
F37	SPARE5	SPARE5
B2	SPARE4	SPARE4
B2	NO_CONNECT6	NO_CONNECT6
B3	NO_CONNECT8	NO_CONNECT8
C2	SPARE1	SPARE1
C3	SPARE2	SPARE2
R41	SPARE6	SPARE6

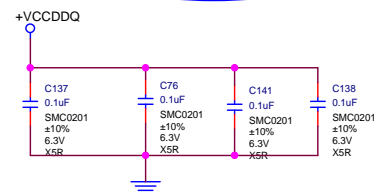
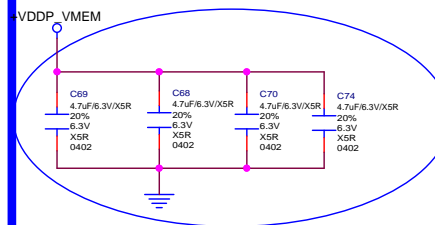


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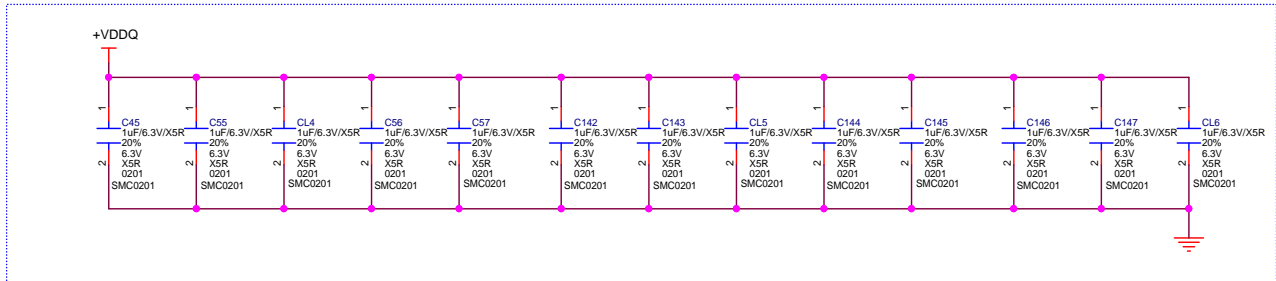
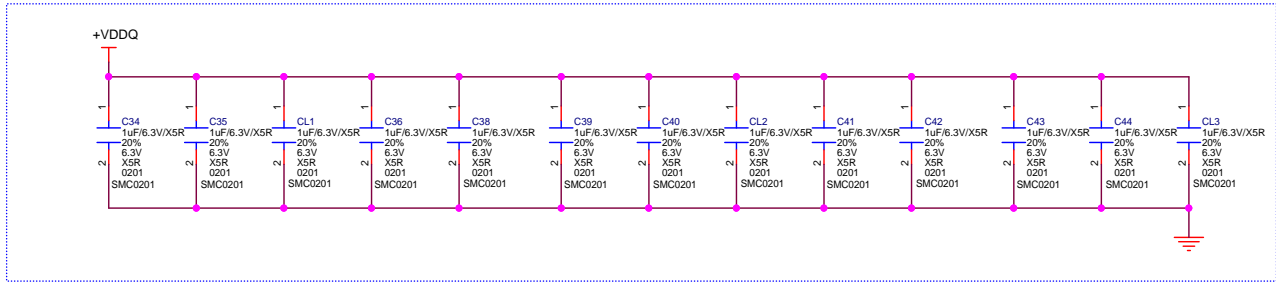





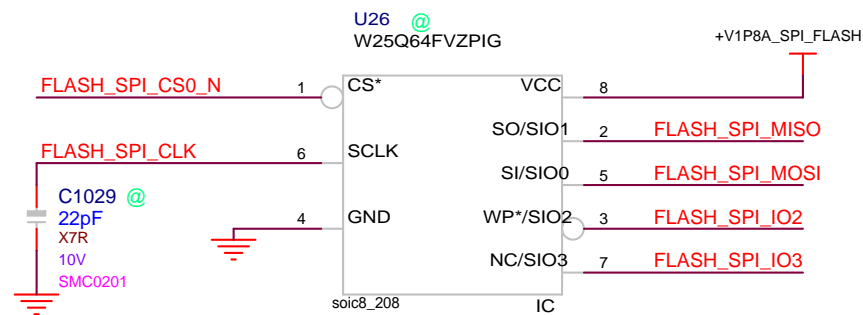
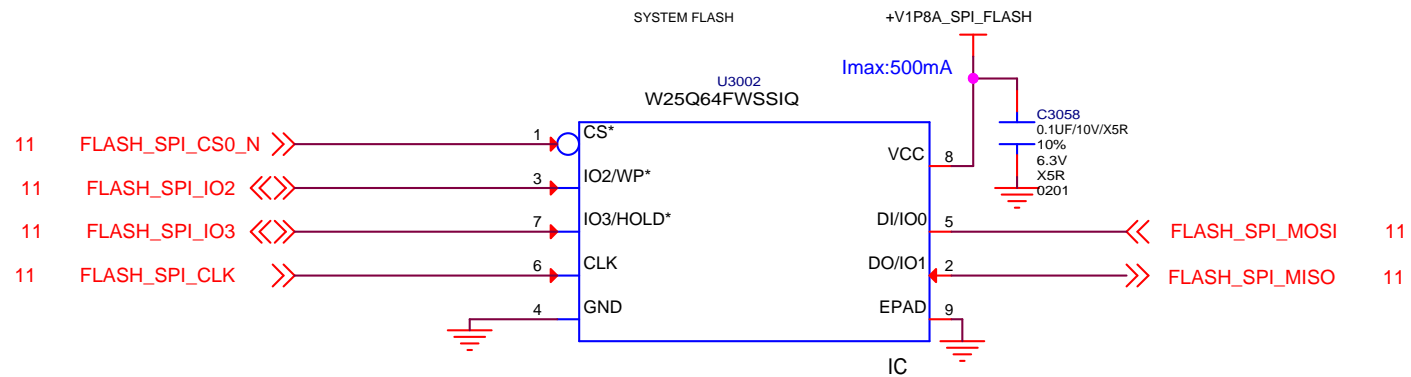
EDGE DECAPS FOR EXPOSED POWER PLANES (EMC FEEDBACK)

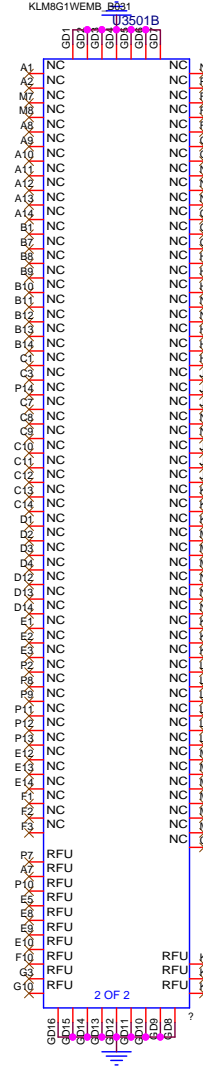
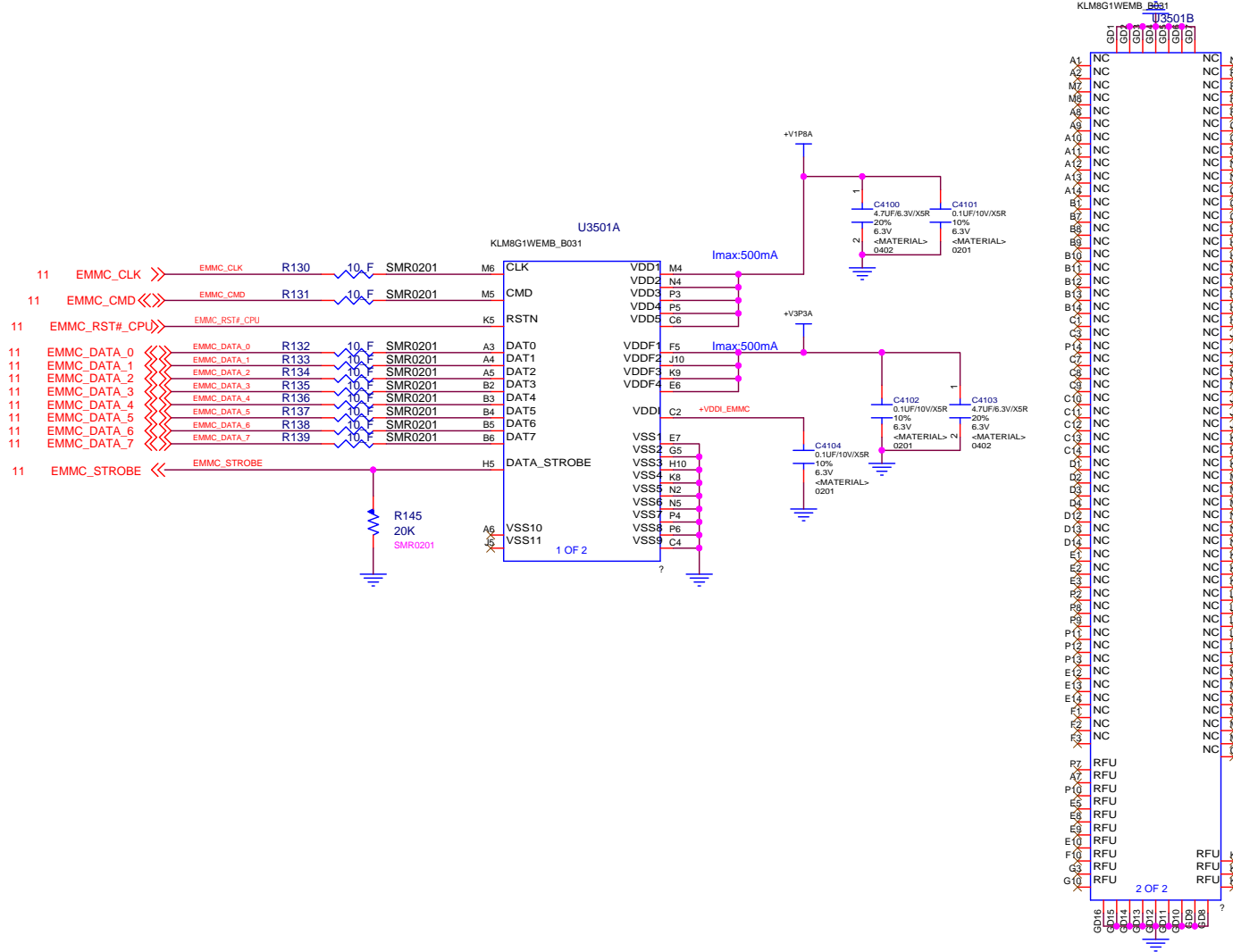



EDGE DECAPS FOR EXPOSED POWER PLANES (EMC FEEDBACK)




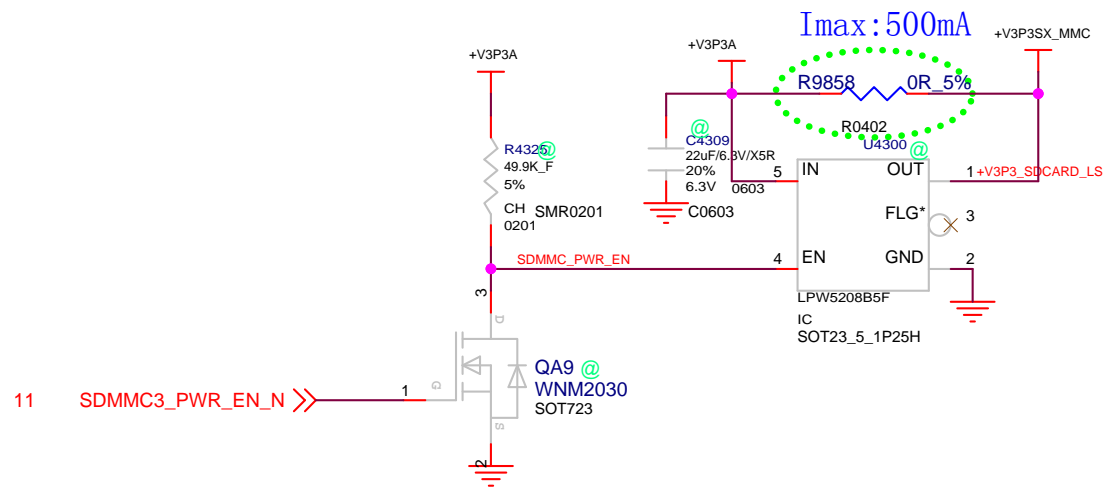
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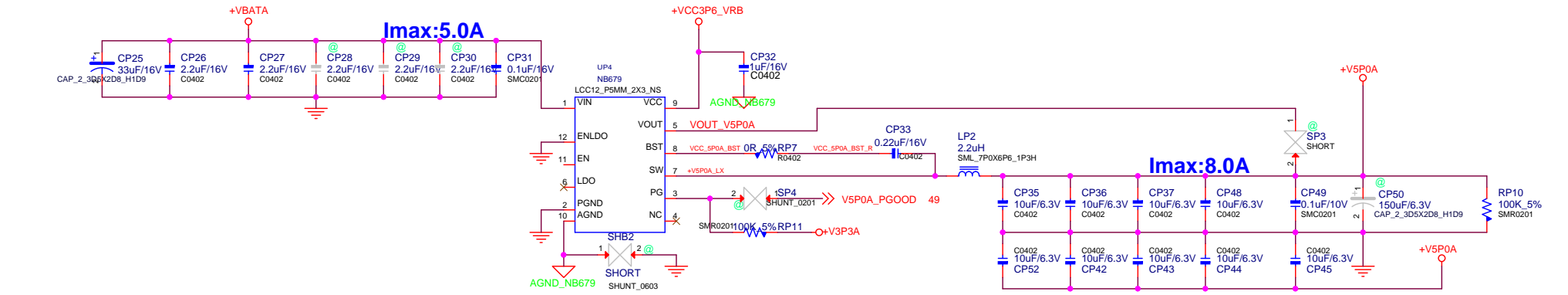
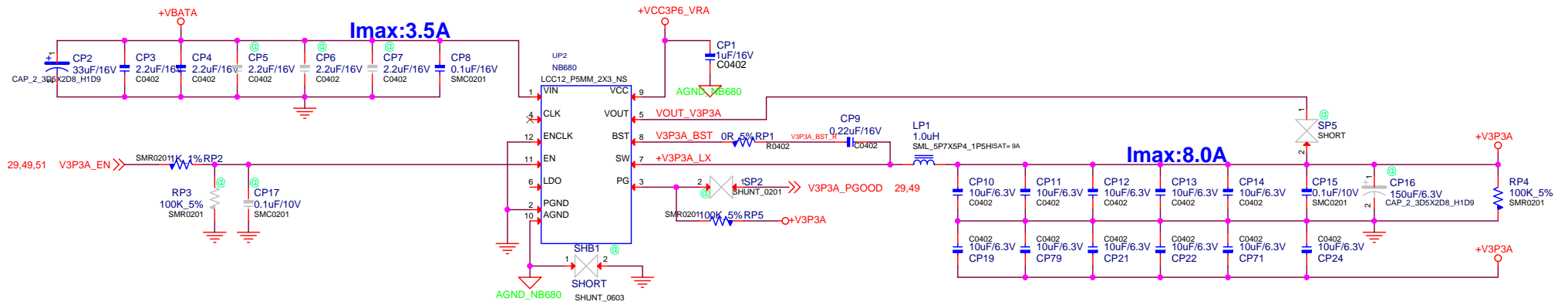



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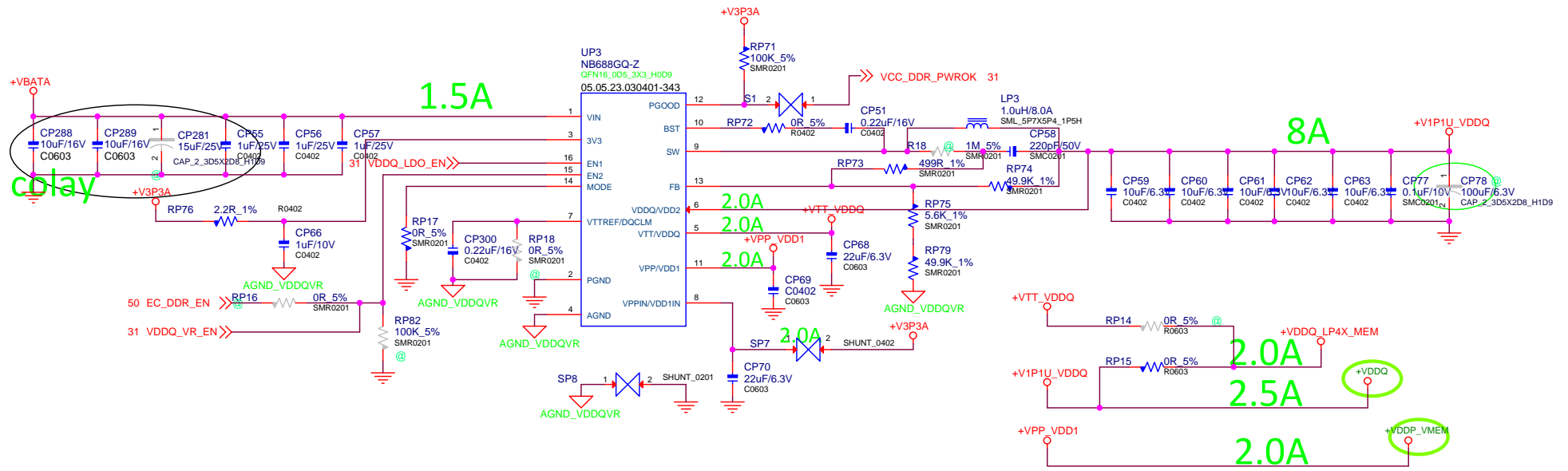
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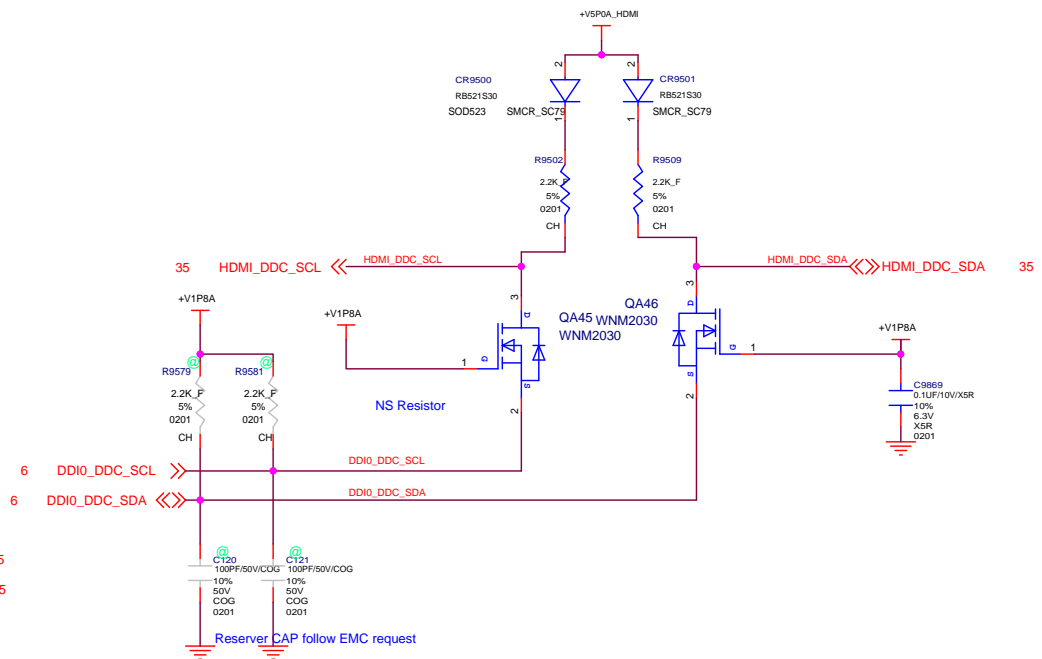
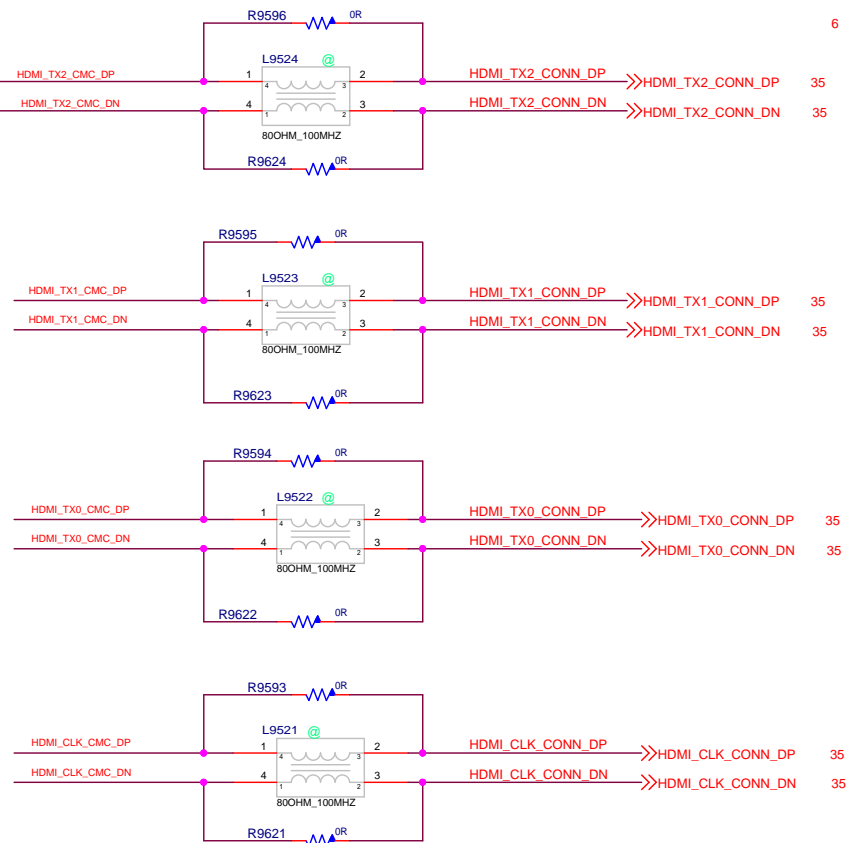
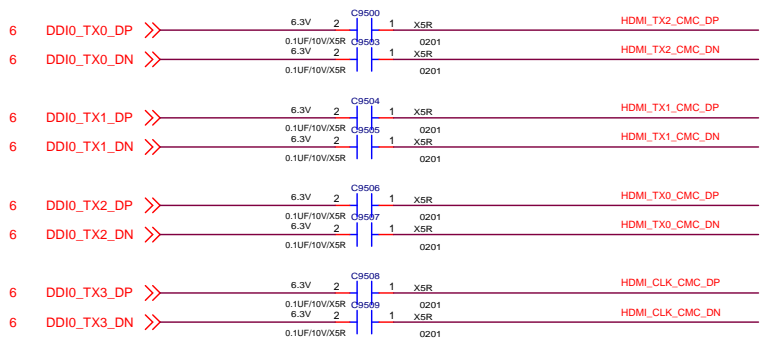
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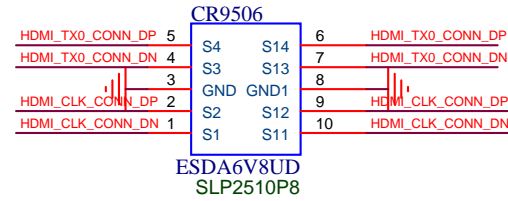
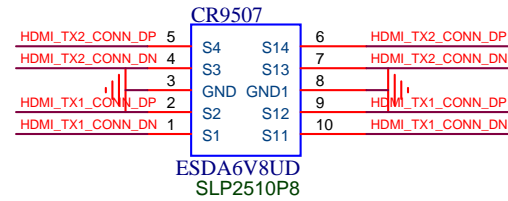
		
Title Gemini Lake		
Size B	Document Number POWER DELIVERY 5V AND 3.3V	Rev R100
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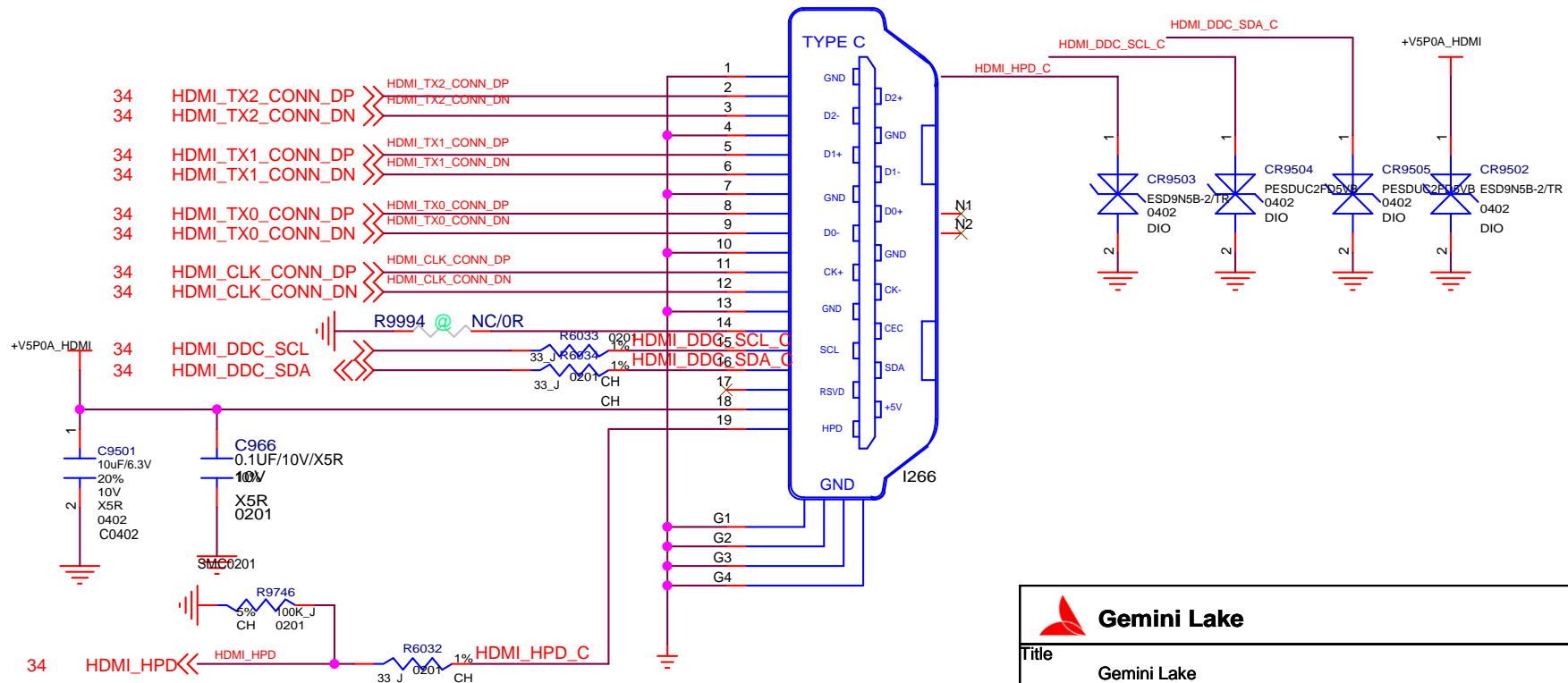
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Title Gemini Lake		
Size B	Document Number NC	Rev R100
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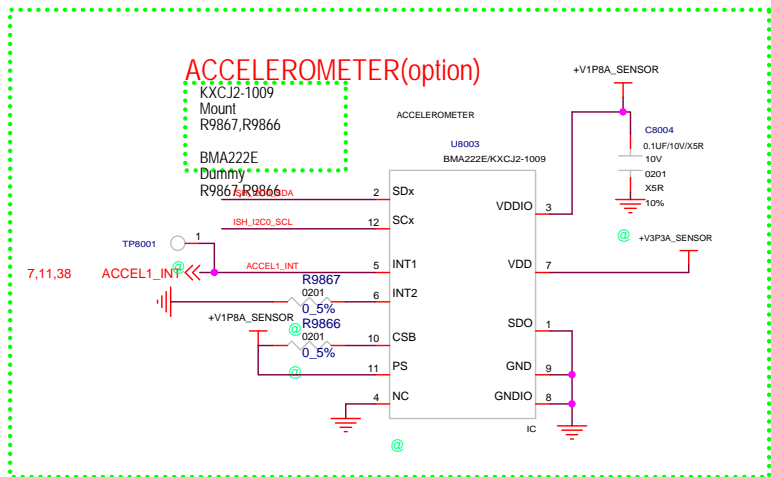
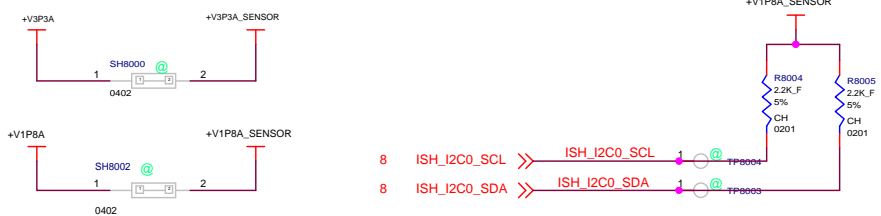
Title Gemini Lake		
Size B	Document Number	Rev R100
HDMI LEVEL SHIFTERS		
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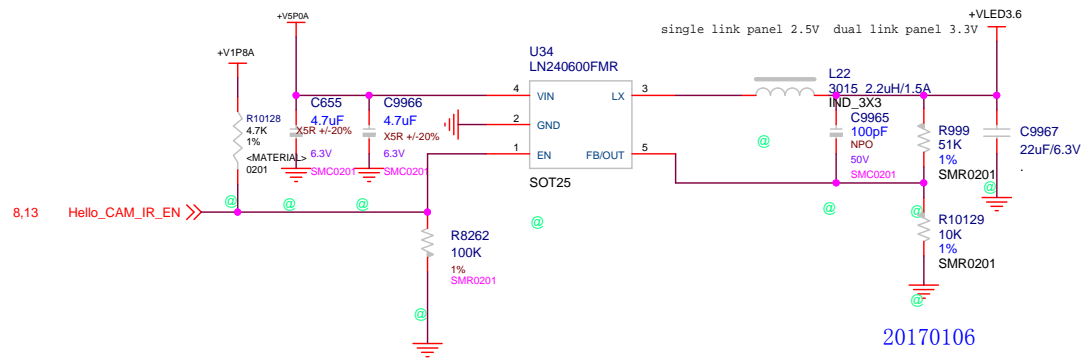
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CON19_P4MM_MICROHDMI
minihdmi_19p_HM16



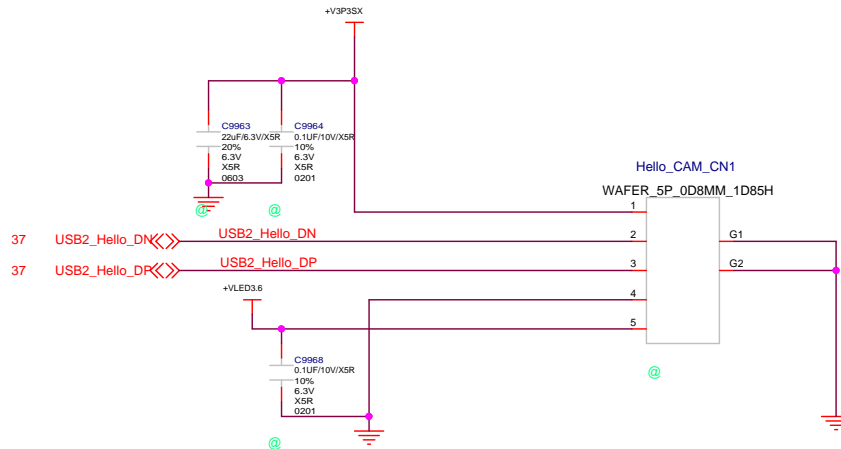
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Title Gemini Lake		
Size A	Document Number HDMI CONNECTOR	Rev R100
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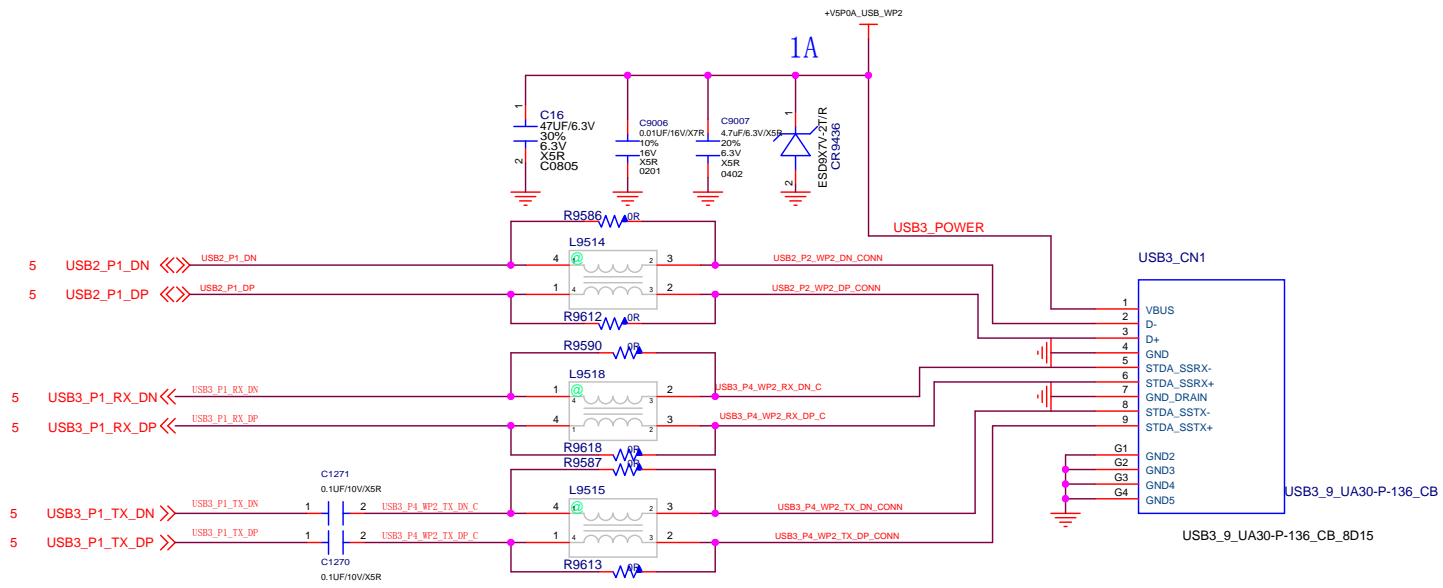
Title Gemini Lake		
Size B	Document Number SENSORS	Rev R100
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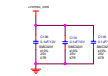
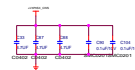
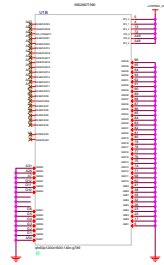
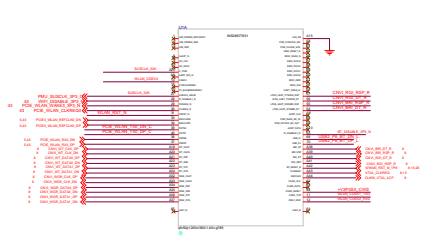
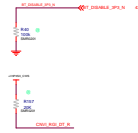
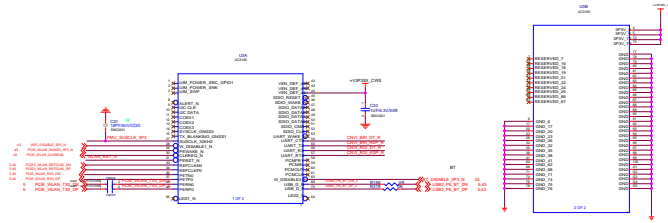
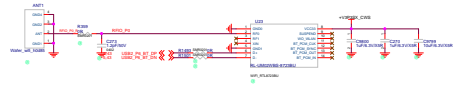
Add Hello CAM

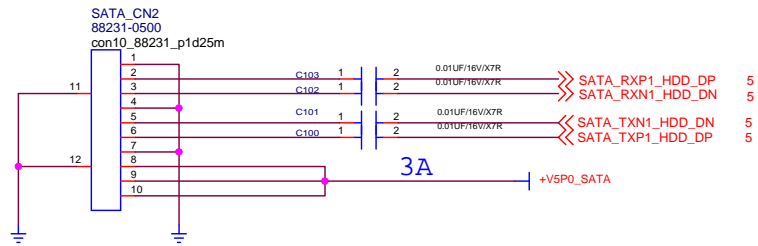



 Gemini Lake		
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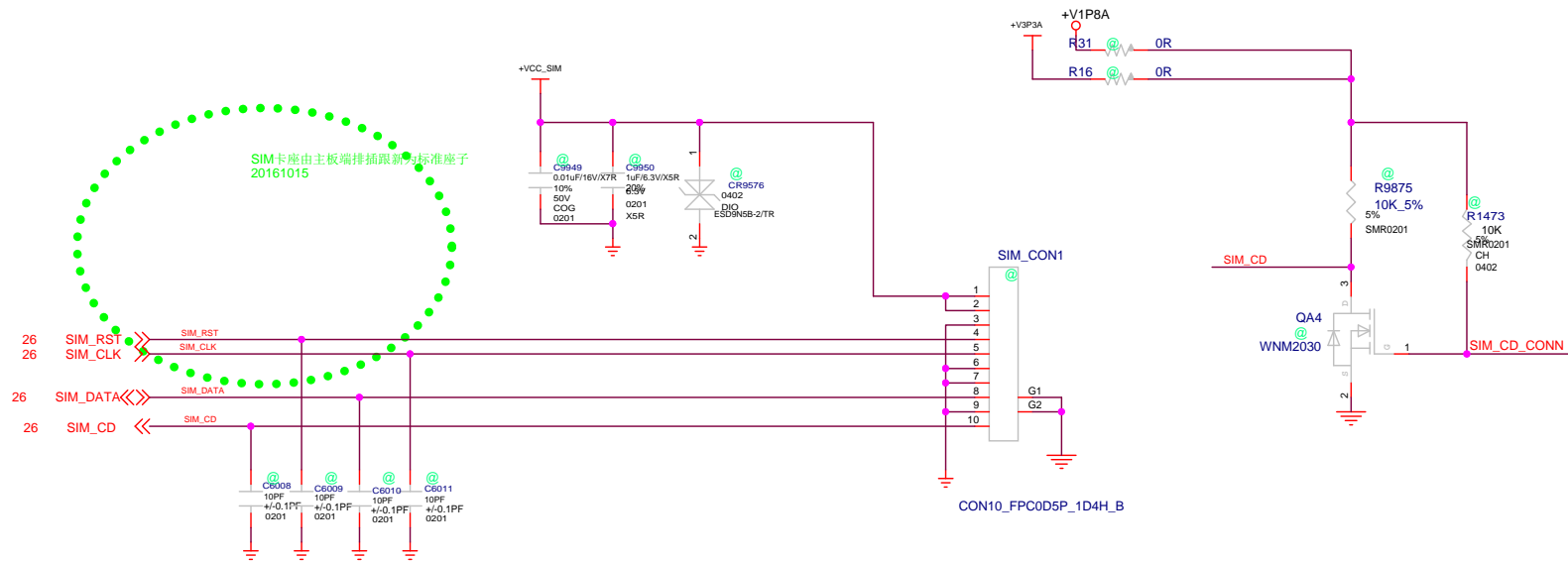



		
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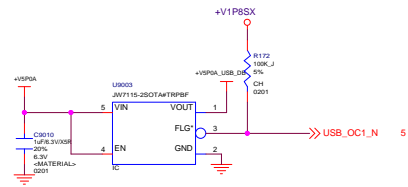




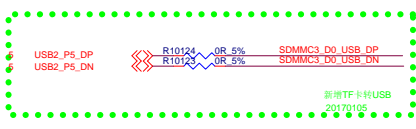
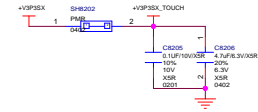
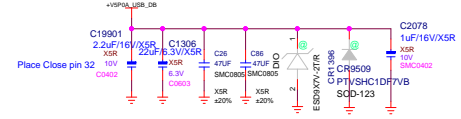
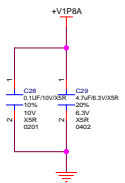
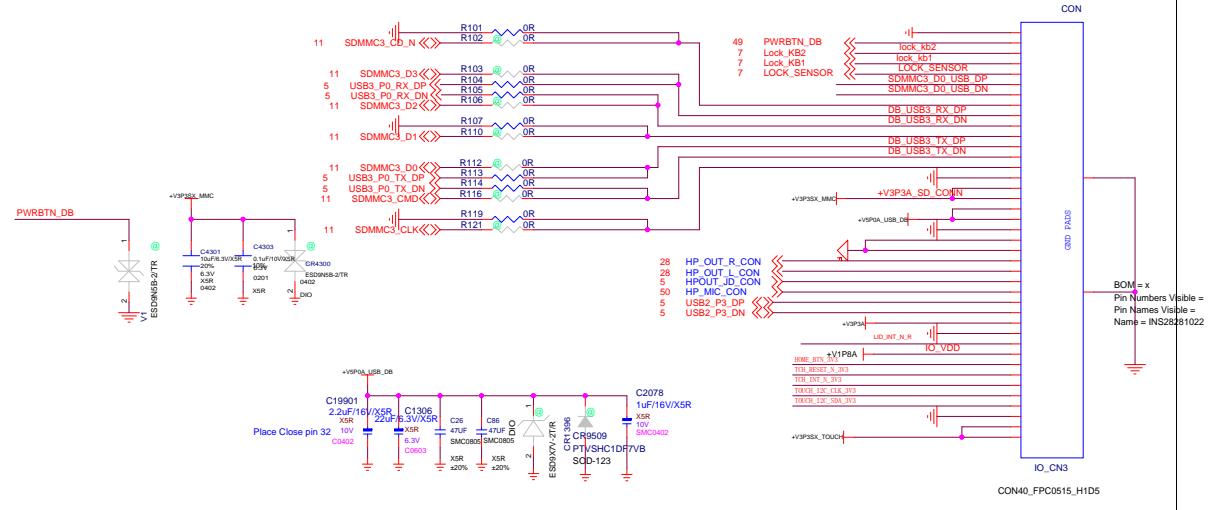
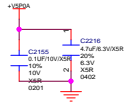
 Gemini Lake		
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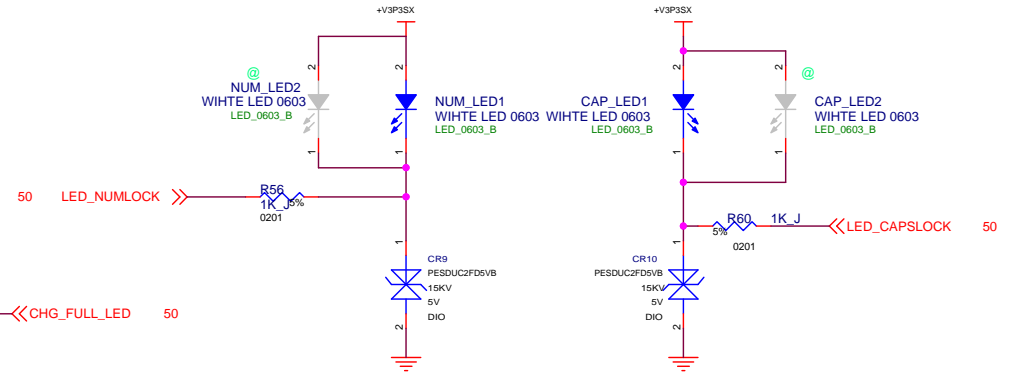
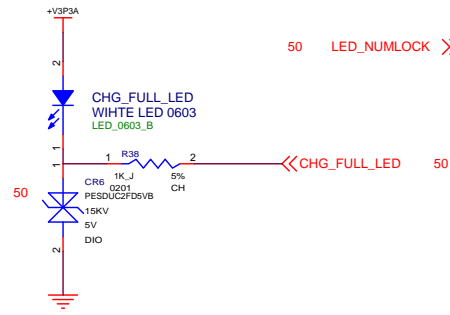
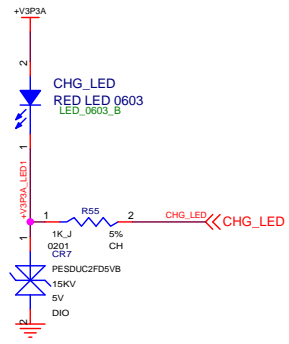
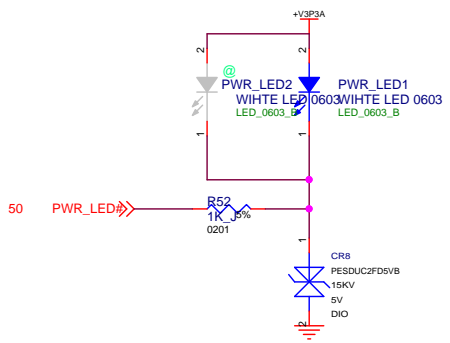



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Change from LPW5208 To WS4601E





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